COMBINED STATEMENT UNDER 37 C.F.R. § 3.73(b)-ESTABLISHING RIGHT OF ASSIGNEE TO TAKE ACTION
and
POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST (REVOCATION OF PRIOR POWERS)

Commissioner for Patents
Washington, D.C. 20231

Sir:

Sun Microsystems, Inc., a corporation organized under the laws of Delaware states that it is the assignee of entire right, title, and interest; in the patents and patent applications listed on the attached **APPENDIX A**, by virtue of an Assignment from the inventor(s) of each of the patents and patent applications listed on the attached APPENDIX A. Date of Recordal, and Reel and Frame number of Recordal by the United States Patent & Trademark Office identified on the attached APPENDIX A.

Sun Microsystems, Inc., a Delaware Corporation, having principle place of business at 901 San Antonio Road, Palo Alto, California, 94303, as assignee of record of the entire interest of the above identified patent (per above statement under 37 CFR §3.73(b)), hereby states that all powers of attorney previously given are hereby revoked.

Further, the following attorneys and/or agents are hereby appointed to prosecute and transact all business in the Patent and Trademark Office connected therewith:

Peter B. Martine (Reg. No. 32,043)
Albert S. Penilla (Reg. No. 39,487)
Rick von Wohld (Reg. No. 48,018)
Chester E. Martine, Jr. (Reg. No. 19,711)
Lygeri Kokkinakos (Reg. No. 42,756)
Kenneth D. Wright (Reg. No. P-53,795)

Edmund H. Mizumoto (Reg. No. 46,938)
Joe A. Brock II (Reg. No. 46,021)
Michael L. Gencarella (Reg. No. 44,703)
George B. Leavell, Esq. (Reg. No. 45,436)
Fariba Yadigar-Bandari (Reg. No. P-53,805)

provided that if any one of said attorneys ceases being affiliated with the law firm of MARTINE & PENILLA, LLP as partner or employee, such attorney's appointment as attorney and all powers derived therefrom shall terminate on the date such attorney ceases being so affiliated; and:

Marc D. Foodman (Reg. No. 34,110)	Monica D. Ward (Reg. No. 40,696)
Timothy J. Crean (Reg. No. 37,116)	Alexander E. Silverman (Reg. No. 37,940)
Naren Chaganti (Reg. No. 44,602)	Marilyn E. Glaubensklee (Reg. No. 35,521)
Anirma R. Gupta (Reg. No. 38,275)	Elaine Lee (Reg. No. 41,936)
Sean P. Lewis (Reg. No. 42,798)	Hugh H. Matsubayashi (Reg. No. 43,779)
Michael J. Schallop (Reg. No. 44,319)	Paul D. Sorkin (Reg. No. 39,039)
Bernice B. Chen (Reg. No. 42,403)	Andrew C. Chen (Reg. No. 43,544)
Noreen A. Krall (Reg. No. 39,734)	Pavel Pogodin (Reg. No. 48,205)
Jeffrey L. Myers (Reg. No. 44,252	

Of Sun Microsystems, Inc., 4120 Network Circle, MS SCA12-203, California, 95054, United States of America.

Please send all correspondence to: **Albert S. Penilla, Esq.**
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Customer Number 25,920

Please direct telephone calls to: **Albert S. Penilla, Esq. at (408) 749-6903.**

The undersigned is authorized to act on behalf of the Assignee.

_____   12/13/02
(Signature of authorized person)   (date)

Marilyn E. Glaubensklee
(type or print name of authorized person)
Assistant General Counsel, having delegated Power
to sign on Behalf of Sun Microsystems, Inc.

Title of authorized person

APPENDIX A

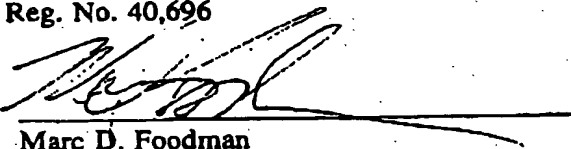| SerialNumber	FilingDate	Status	ReelAndFrame	Title	CaseNumber	ClientNumber
09/499,264	2/7/2000	Issued	010810/0847	METHOD TO REDUCE MEMORY LATENCIES BY PERFORMING TWO LEVELS OF SPECULATION	SUNMP179	P4160/MG
10/066,384	1/31/2002	Pending	012563/0258	ULTRA FAST ADDER	SUNMP180	P6402/MG
10/020,565	12/7/2001	Pending	012390/0405	METHOD TO REDUCE MEMORY LATENCIES BY ALLOWING READS TO BYPASS WRITES	SUNMP181	P6440/MG
10/024,884	12/18/2001	Pending	012397/0344	OUTPUT DRIVER WITH COMPENSATED SLEW RATE AND DELAY CONTROL	SUNMP182	P6914/MG
09/520,921	3/8/2000	Issued	010662/0415	METHOD FOR SIZING PMOS PULL-UP DEVICES	SUNMP183	P4283/MG
09/669,829	9/26/2000	Pending	011468/0692	METHOD AND APPARATUS FOR HARDWARE MULTITHREADING	SUNMP185	P4737/MG
09/815,227	3/22/2001	Pending	11639/0759	HIGH SPEED LOW POWER CONTENT ADDRESSABLE MEMORY	SUNMP186	P4895/MG
09/838,921	4/20/2001	Pending	11959/0552	A METHOD TO DYNAMICALLY ALLOCATE PARTS OF ON CHIP L2 CACHE TO EXECUTING THREADS ON A CHIP MULTIPROCESSOR	SUNMP187	P4944/MG
09/670,856	9/26/2000	Pending	11513/0043	SIMPLIFIED WRITEBACK HANDLING	SUNMP188	P4950/MG
09/839,057	4/20/2001	Pending	11964/0743	YIELD IMPROVEMENT THROUGH PROBE-BASED CACHE SIZE REDUCTION	SUNMP189	P4952/MG
10/086,911	2/28/2002	Pending	12681/0529	REGISTER-STACK IN D-CACHE	SUNMP190	P6919/MG
10/113,365	3/29/2002	Pending	12766/0648	DELAY TEST DFT FOR TAP-BASED DESIGNS	SUNMP191	P7072/MG
10/189,870	7/3/2002	Pending	013088/0744	HIERARCHICAL BIST METHODOLOGY FOR MULTI-CORE CHIPS	SUNMP192	P7404/MG
09/375,700	8/16/1999	Issued	010182/0642	MERGING SINGLE PRECISION FLOATING POINT OPERANDS	SUNMP194	P3656/MG
09/543,372	4/2/2000	Issued	010724/0734	DUAL RAIL DYNAMIC FLIP-FLOP WITH SINGLE EVALUATION PATH	SUNMP195	P4136/MG
08/884,257	6/27/1997	Issued	9000/0551	PROCESSOR COMPLEX FOR EXECUTING MULTIMEDIA FUNCTIONS	SUNMP229	P2257/MG
09/007,525	1/15/1998	Issued	8953/0916	METHOD OF PRODUCING A SEQUENCE OF TRIANGLES FROM A VERTEX RASTER WITH AND WITHOUT HALF RESOLUTION EDGES WHILE DECOMPRESSING A COMPRESSED GEOMETRY	SUNMP215	P2812/MG
09/590,935	6/8/2000	Pending	010857/0493	SYNCHRONIZATION FOR PHASE MISMATCH	SUNMP216	P3159/MG
08/884,841	6/30/1997	Issued	9229/0713	NON-BLOCKING DELAYED CLOCKING SCHEME FOR DOMINO LOGIC	SUNMP218	P2335/MG
08/884,840	6/30/1997	Issued	9240/0077	METHOD FO GENERATING NON-BLOCKING DELAYED CLOCKING SIGNALS FOR DOMINO LOGIC	SUNMP219	P2545/MG
10/122,101	4/11/2002	Pending	012806/0467	METHOD TO STORE CROSS HIERARCHY COUPLING DATA IN A HIERARCHICAL EXTRACATED DATA	SUNMP222	P6123/MG
10/120,075	4/9/2002	Pending	012794/0763	SOFTWARE-CONTROLLABLE REGISTER MAP	SUNMP223	P6417/MG

DELEGATION OF SIGNING AUTHORITY

I, Marc D. Foodman, Chief Patent Counsel, of Sun Microsystems Inc., (the "Company"), hereby appoint each of the following persons listed below as Attorney-in-Fact of the Company to execute and deliver in the name and on behalf of the Company patent office documents and to grant powers of attorney to patent attorneys and patent agents representing the Company in the patent offices and trademark office throughout the world as required to facilitate filing, obtaining, renewing, maintaining, working, opposing and terminating patent applications and patent registrations on behalf of the Company, and to execute documents which effect the transfer of title or licenses of patents or trademark rights so long as such transfers are between any of the Company and its subsidiaries. This Delegation Of Signing Authority is effective as of August 1, 2002 and will continue in full force and effect while such persons are employees of the Company or until such time as I otherwise designate.

Andrew C. Chen, Reg. No. 43,544
Bernice B. Chen, Reg. No. 42,403
Marilyn E. Glaubensklee, Reg. No. 35,521
Anirma R. Gupta, Reg. No. 38,275
Noreen A. Krall, Reg. No. 39,734
Elaine Lee, Reg. No. 41,936
Sean P. Lewis, Reg. No. 42,798
Jeffrey L. Myers, Reg. No. 44,252
Pavel Pogodin, Reg. No. 48,205
Alexander E. Silverman, Reg. No. 37,940
Paul D. Sorkin, Reg. No. 39,039
Monica D. Ward Reg. No. 40,696

Marc D. Foodman
Chief Patent Counsel
Sun Microsystems, Inc.

APPENDIX B